

PSA: 2.5mm x 27mm

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Crystalfontz



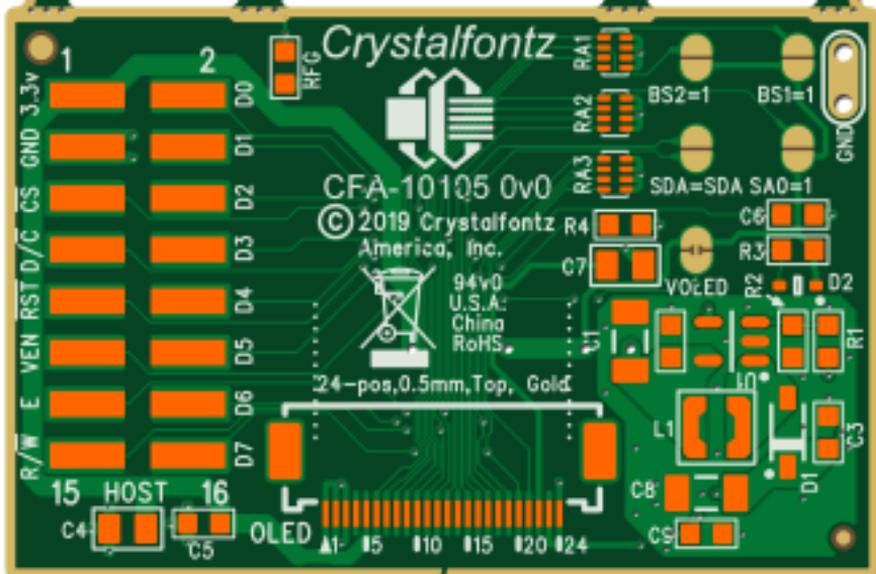
CFA-10105 0v0

Carrier board for:

- CFAL12856A0-0151 (Transparent OLED)
- CFAL12864D-0154M

Interfaces:

- "4-wire" SPI
- I²C
- "8080" 8-bit Parallel
- "6800" 8-bit Parallel



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BS2*	BS1*	INTERFACE	D7~D3*	D2	D1	D0	E(RD)*	R/W(WR)*	CS*	D/C*
0	0	*4-Wire SPI	Tie Low*	NC	MOSI	SCK	Tie Low*		CS	D/C
0	1	I2C(close SDA=SDA)	Tie Low*	SDA=SDA	SCL		Tie Low*		SA0	
1	0	8-Bit Par (E,R/W)	D7~D3	D2	D1	D0	E	R/W	CS	D/C
1	1	8-Bit Par (RD,WR)	D7~D3	D2	D1	D0	RD	WR	CS	D/C

* BS1,BS2,CS,D/C,R/WR,E/RD & D7~D3 have 100K pull-downs

SA0	I2C Address		
	Write	Read	R/W
0	0x78	0x70	0x3C
1	0x7A	0x7B	0x30

PCB10105 0v0
 CFA-10105 0v0
 DP / PRODUCT LABEL



94v0
 U.S.A.
 China
 RoHS

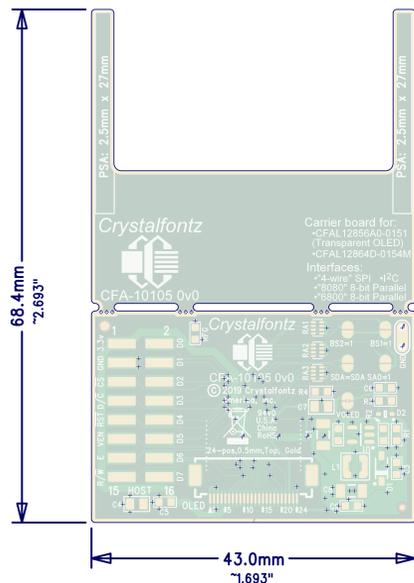
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LAYERS: 2
 MATERIAL: FR4, RoHS, 94v0
 THICKNESS: 0.8mm (~31 mils)
 FINISH: Gold (ENIG) RoHS
 SILK SCREEN: White
 SOLDER MASK: Green

MINIMUM SPACING: 0.1 mm (3.937 mils)
 MINIMUM TRACE: 0.1 mm (3.937 mils)
 SMALLEST VIA: 0.3 mm drill in 0.5 mm pad
 (11.81 mil drill in 19.69 mil pad)

>> IMPORTANT FABRICATION NOTES <<

- 1) Many pads have intentional undersize solder mask openings/oversize pads for physical strength.
- 2) Make top and bottom solder masks exactly as shown. Do NOT "open-up" the solder mask layers. This design uses "solder-mask-defined-pads".
- 3) Silkscreened Legend (white) must be clean and clear. Silkscreen legend must have good registration. Silkscreen is intentionally placed over bare copper. Do not clip silkscreen to solder mask layer.
- 4) Edges of board must be smooth. Use router. Do not use "score and break".
- 5) Board must be exactly like:
 CFA10105_0v0_PCB_Preview.pdf



SIZE	QTY	SYM	PLATED	TOL
0.3	82	+	YES	+/-0.0
0.3	12	◇	NO	+/-0.0
0.508	4	+A	YES	+/-0.0
0.9	2	+B	YES	+/-0.0

BARE TRACE BEYOND EDGE IS INTENTIONAL
 ESD HANDLING FEATURE. ROUTING WILL
 TRIM COPPER TO EXACT BOARD OUTLINE.
 SEE "CFA10105_0v0_PCB_Preview.pdf"
 Edge plating is not required.

 Crystalfontz 12412 East Saltese Avenue Spokane Valley, WA 99216 USA Tel: (509) 892-1200 http://www.crystalfontz.com Copyright © 2019 Crystalfontz America, Inc.	PROJECT	CFA-10105		VERSION	0v0
	PCB	REVISION	0v0	DATE	2019-11-11
LAYER	Drill Drawing/Notes		FILE NAME	C- CFA10105_0v0_DD.pho	

Edge is
not plated

Copper goes
all the way
to the edge

