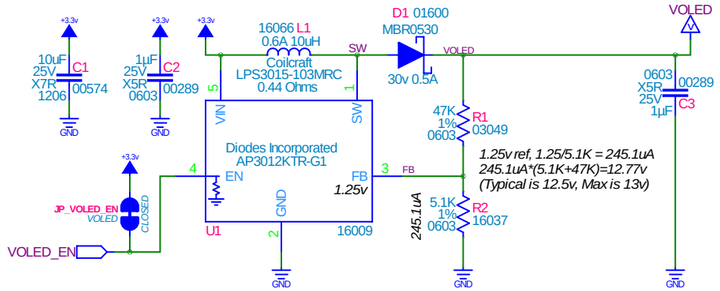
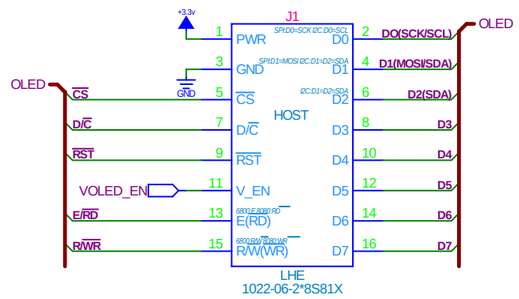
REV	ENGINEER	DATE	REMARKS
0v0	BAC	2019-11-11	Initial Creation
-	-	-	-
-	-	-	-
-	-	-	-
-	-	-	-

3.3v to 12.77v OLED Boost Supply



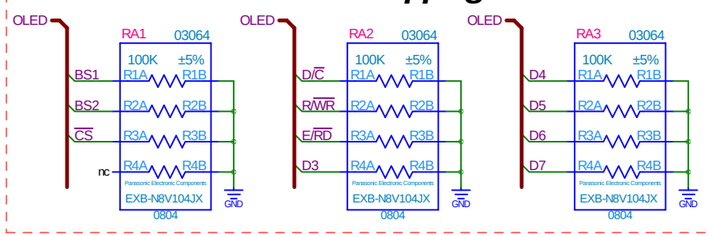
Host Connector



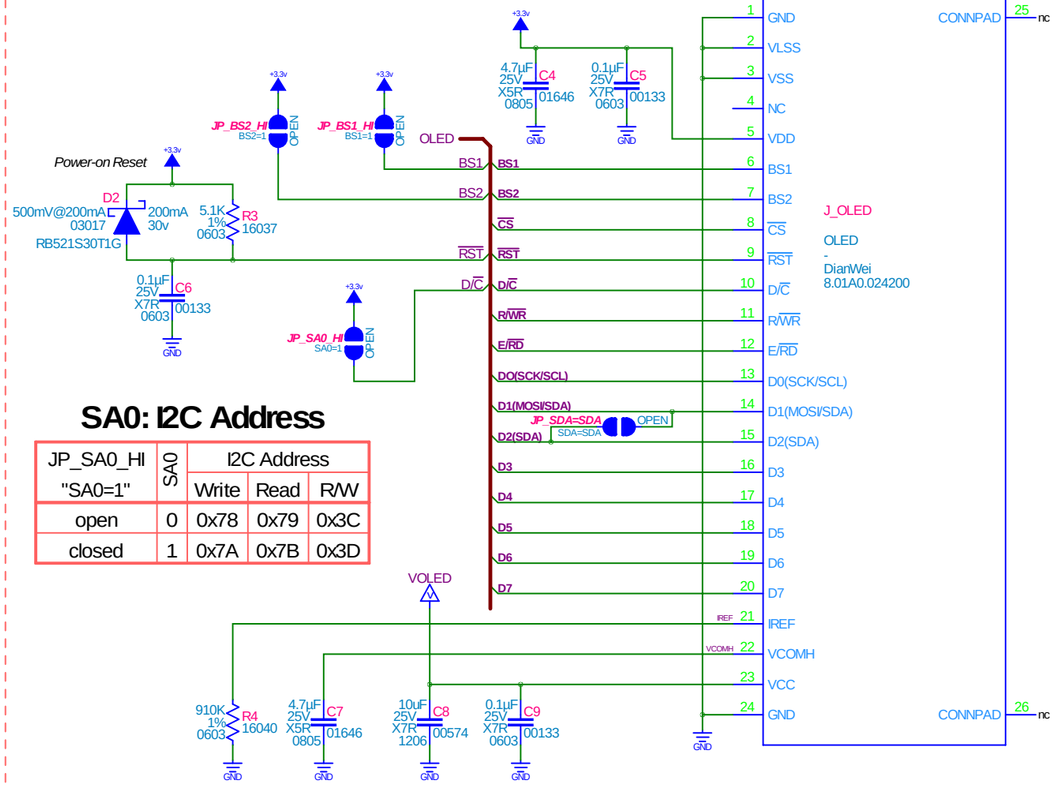
Jumper Setting for Interface Selection

JP_BS2_HI "BS2=1"	JP_BS1_HI "BS1=1"	BS2	BS1	Interface	Pin Functions							
					D7 D6 D5 D4 D3	D2	D1	D0	E(RD)	R/W(WR)	CS	D/C
open	open	0	0	"4-wire" 8-bit SPI (default)	Tie Low*	NC	MOSI	SCK	Tie Low*		CS	D/C
open	closed	0	1	I2C	Tie Low*	SDA=SDA (close "SDA=SDA" jumper)		SCL	Tie Low*			SA0
closed	open	1	0	"6800" 8-bit Parallel (E, R/W)	D7 D6 D5 D4 D3	D2	D1	D0	E	R/W	CS	D/C
closed	closed	1	1	"8080" 8-bit Parallel (RD, WR)	D7 D6 D5 D4 D3	D2	D1	D0	RD	WR	CS	D/C

* Pull 'Tie Low' & Strapping lines down.



OLED Components & ZIF Connector

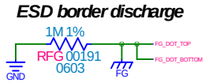


SA0: I2C Address

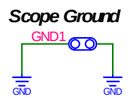
JP_SA0_HI "SA0=1"	SA0	I2C Address		
		Write	Read	R/W
open	0	0x78	0x79	0x3C
closed	1	0x7A	0x7B	0x3D

Perforations for break-away

ESD border discharge



Scope Ground



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CFA-10105: Transparent OLED Carrier Board

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PRODUCT NAME:	PRODUCT REVISION:	PCB NUMBER:	PCB REVISION:
CFA-10105	0v0	PCB10105	0v0