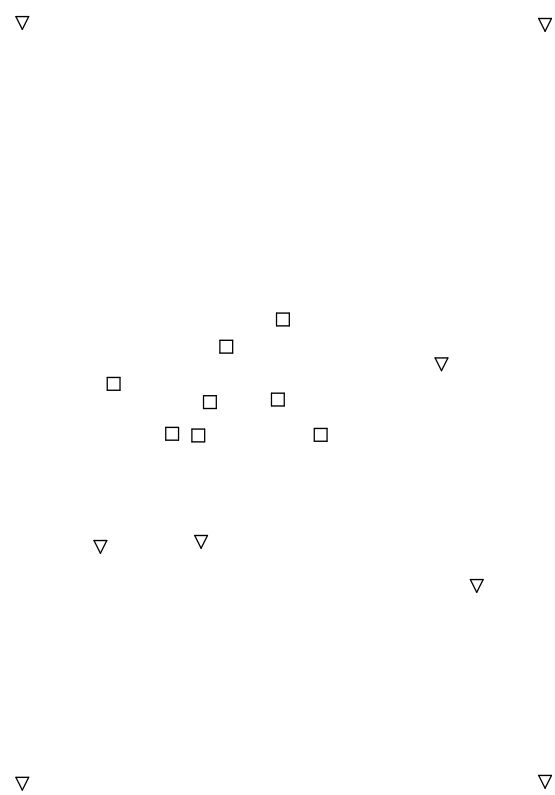



Symbol	Count	Hole Size	Plated	Hole Type	Drill Layer Pair	Via/Pad
□	8	19.69mil (0.500mm)	PTH	Round	Top Layer - Bottom Layer	Via
▽	8	23.62mil (0.600mm)	PTH	Round	Top Layer - Bottom Layer	Via
	16 Total					

LAYERS: 2
MATERIAL: FR4, RoHS, 94v0
THICKNESS: 1.6mm (~62 mils)
FINISH: Gold (ENIG) RoHS
SILK SCREEN: White
SOLDER MASK: GREEN

>> IMPORTANT FABRICATION NOTES <<

- 1) Many pads have intentional undersize solder mask openings/oversize pads for physical strength.
- 2) Make top and bottom solder masks exactly as shown. Do _NOT_ "open-up" the solder mask layers. This design uses "solder-mask-defined-pads".
- 3) Silkscreened Legend (white) must be clean and clear. Silkscreen legend must have good registration. Silkscreen is intentionally placed over bare copper. Do not clip silkscreen to solder mask layer.
- 4) Board must be exactly like: N/A





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PROJECT

CFA-10112

VERSION

h0v2

PCB

CFA10112

REVISION

h0v2

DATE

2021-03-31

LAYER

Drill Drawing/Notes

FILE NAME

CFA10112 .GD1



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PROJECT	CFA-10112	VERSION	h0v2
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PCB	REVISION	DATE
CFA10112	h0v2	2021-03-31

LAYER
Drill Guide

FILE NAME
CFA10112.GG1

