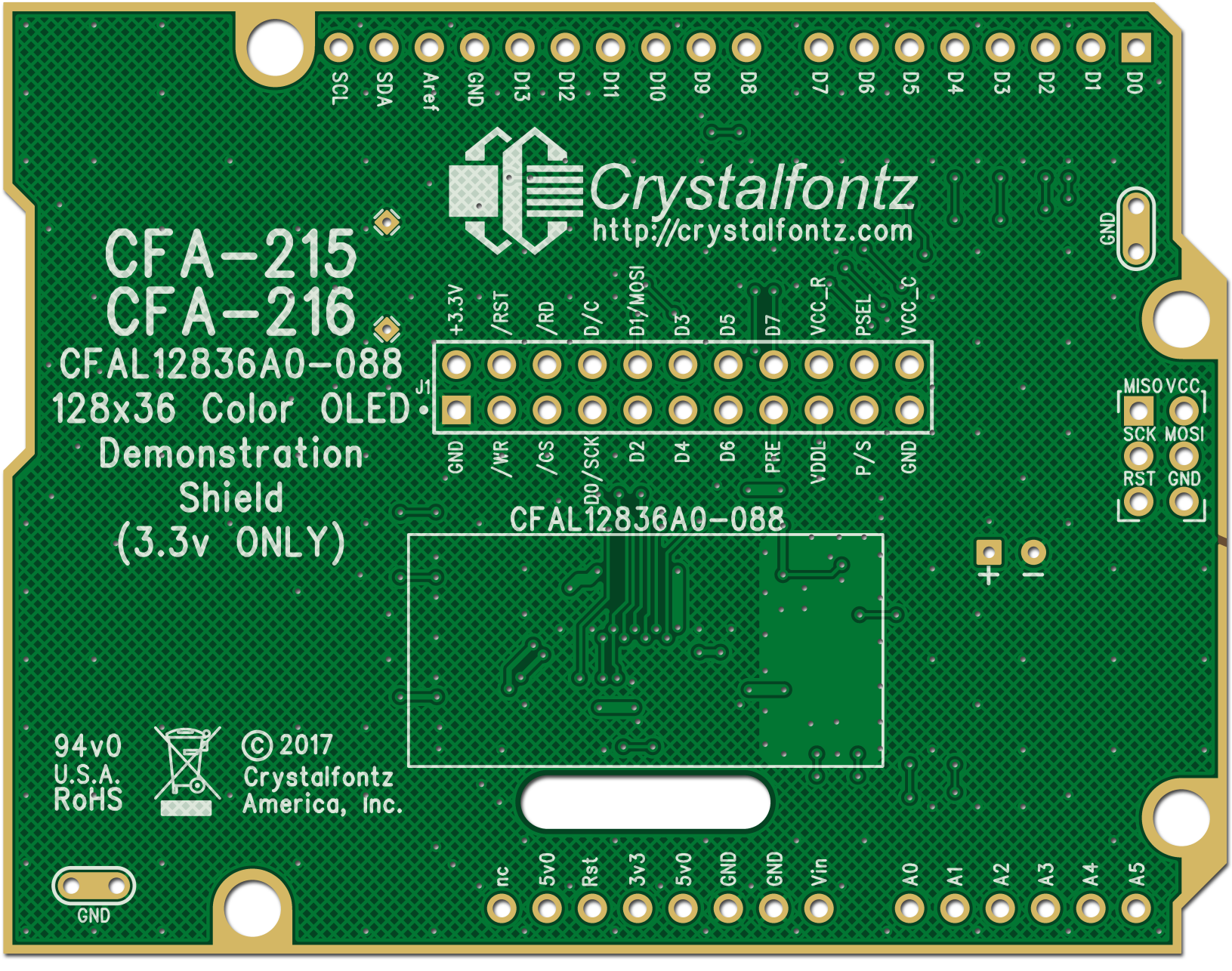
SCL
SDA
Aref
GND
D13
D12
D11
D10
D9
D8
D7
D6
D5
D4
D3
D2
D1
D0
Crystalfontz
http://crystalfontz.com
GND
CFA-215
CFA-216
CFAL12836A0-088
128x36 Color OLED
Demonstration
Shield
(3.3v ONLY)
J1
+3.3V
/RST
/RD
D/C
D1/MOSI
D3
D5
D7
VCC_R
PSEL
VCC_C
GND
/WR
/CS
D0/SCK
D2
D4
D6
PRE
VDDL
P/S
GND
MISO
VCC
SCK
MOSI
RST
GND
CFAL12836A0-088
94v0
U.S.A.
RoHS
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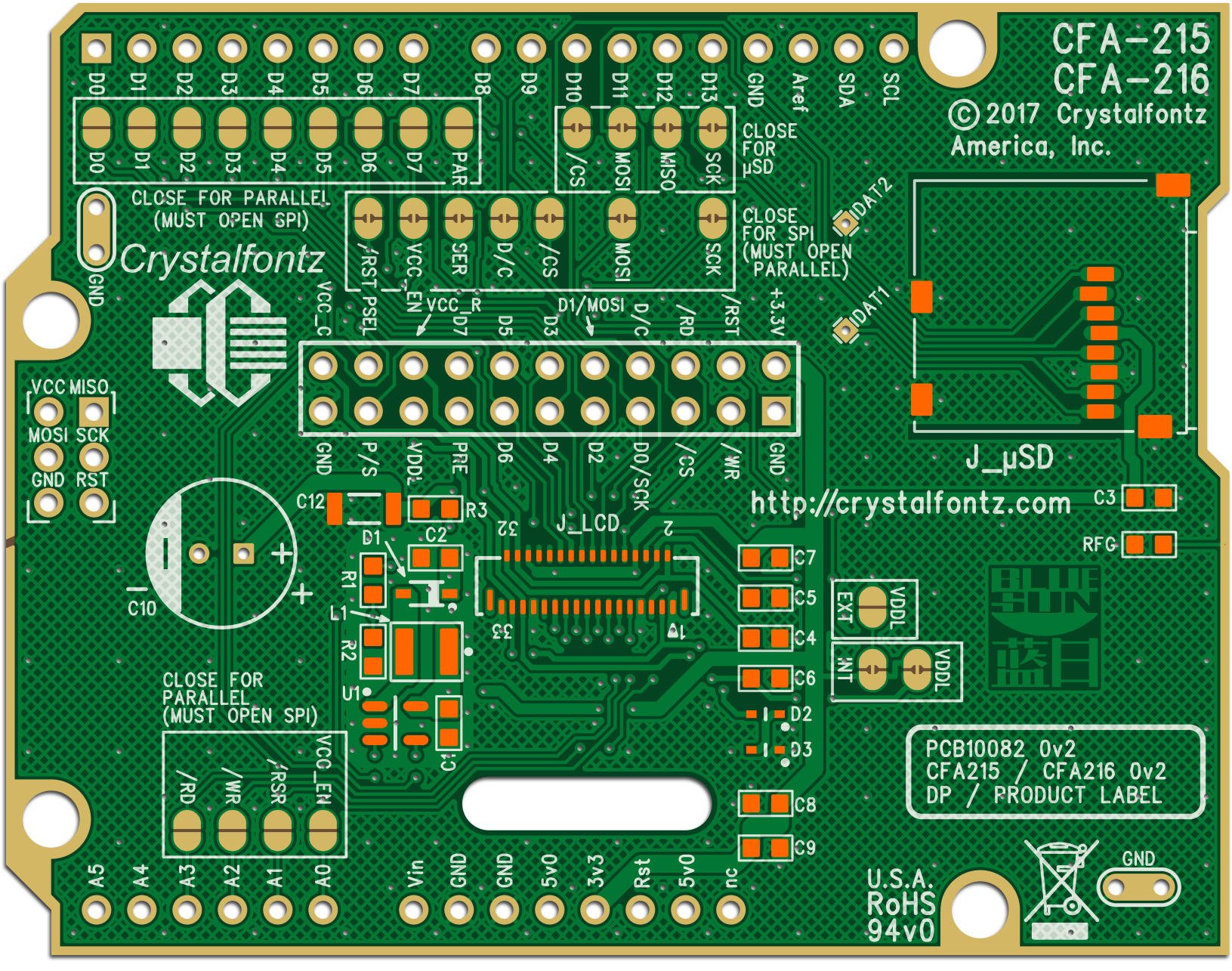
Crystalfontz
America, Inc.
GND
nc
5v0
Rst
3v3
5v0
GND
GND
Vin
A0
A1
A2
A3
A4
A5

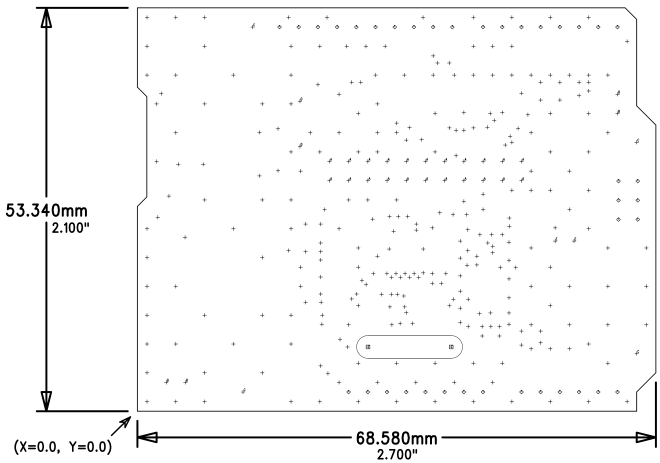
CFA-215
CFA-216
© 2017 Crystalfontz
America, Inc.
J_hSD
http://crystalfontz.com
J_LCD
CLOSE FOR PARALLEL
(MUST OPEN SPI)
CLOSE FOR SPI
(MUST OPEN PARALLEL)
CLOSE FOR hSD
PCB1008Z 0v2
CFA215 \ CFA216 0v2
DP \ PRODUCT LABEL
U.S.A.
RoHS
94v0


CFA-215
CFA-216
© 2017 Crystalfontz America, Inc.
D0 D1 D2 D3 D4 D5 D6 D7 D8 D9 D10 D11 D12 D13 GND Aref SDA SCL
CLOSE FOR PARALLEL (MUST OPEN SPI)
CLOSE FOR µSD
CLOSE FOR SPI (MUST OPEN PARALLEL)
Crystalfontz
/RST PSEL VCC_EN SER D/C /CS MOSI SCK
VCC_C VCC_R D7 D5 D3 D1/MOSI D/C /RD /RST +3.3V
GND P/S VDD2 PRE D6 D4 D2 D0/SCK /CS /WR GND
DAT2
DAT1
J_µSD
http://crystalfontz.com
VCC MISO MOSI SCK GND RST
C12 R3 C2 D1 R1 L1 R2 U1 C1 C10
J_LCD
C7 C5 C4 C6 D2 D3 C8 C9
C3 RFG
VDDL EXT VDDL INT
BLUE SUN 蓝日
PCB10082 0v2
CFA215 / CFA216 0v2
DP / PRODUCT LABEL
CLOSE FOR PARALLEL (MUST OPEN SPI)
/RD /WR /RSR VCC_EN
A5 A4 A3 A2 A1 A0 Vin GND GND 5v0 3v3 Rst 5v0 nc
U.S.A.
RoHS
94v0
GND

LAYERS: 2
MATERIAL: FR4, RoHS, 94v0
THICKNESS: 1.2mm (~50 mils)
MINIMUM SPACING: 0.1778 mm (7 mils)
MINIMUM TRACE: 0.1778 mm (7 mils)
SMALLEST VIA: 0.3 mm drill in 0.6 mm pad
(11.81 mil drill in 23.62 mil pad)
FINISH: Gold (ENIG) RoHS
SILK SCREEN: White
SOLDER MASK: Green

>> IMPORTANT FABRICATION NOTES <<

1) Many pads have intentional undersize solder mask openings/oversize pads for physical strength.
2) Make top and bottom solder masks exactly as shown. Do _NOT_ "open-up" the solder mask layers. This design uses "solder-mask-defined-pads".
3) Silkscreened Legend (white) must be clean and clear. Silkscreen legend must have good registration.
4) Edges of board must be smooth. Use router. Do not use "score and break". Mouse bites OK for panel. Check: CFA10082_0v2_PCB_Preview.pdf



SIZE	QTY	SYM	PLATED	TOL
0.3	293		YES	+/-0.0
1	38		YES	+/-0.0
1.05004	22	A	YES	+/-0.0
0.9	4	B	YES	+/-0.0
3.175	4	C	YES	+/-0.0
0.508	2	D	YES	+/-0.0
0.65	2	E	YES	+/-0.0
3 x 14	1	Z	NO	+/-0.0

BARE TRACE BEYOND EDGE IS INTENTIONAL
ESD HANDLING FEATURE. ROUTING WILL
TRIM COPPER TO EXACT BOARD OUTLINE.
SEE PDF "BOARD PREVIEW" FOR EXAMPLE.

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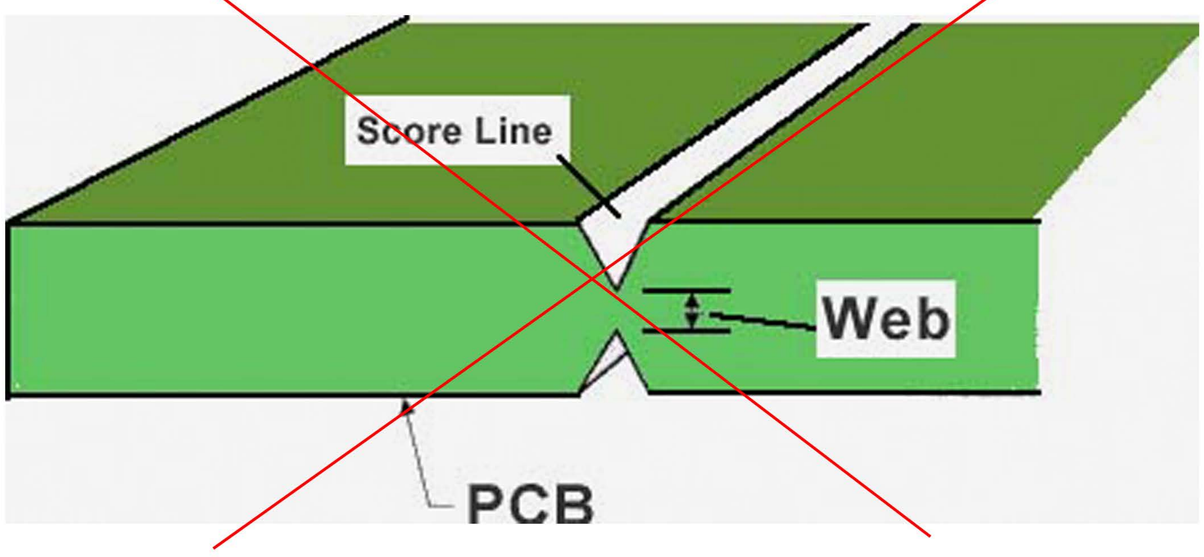
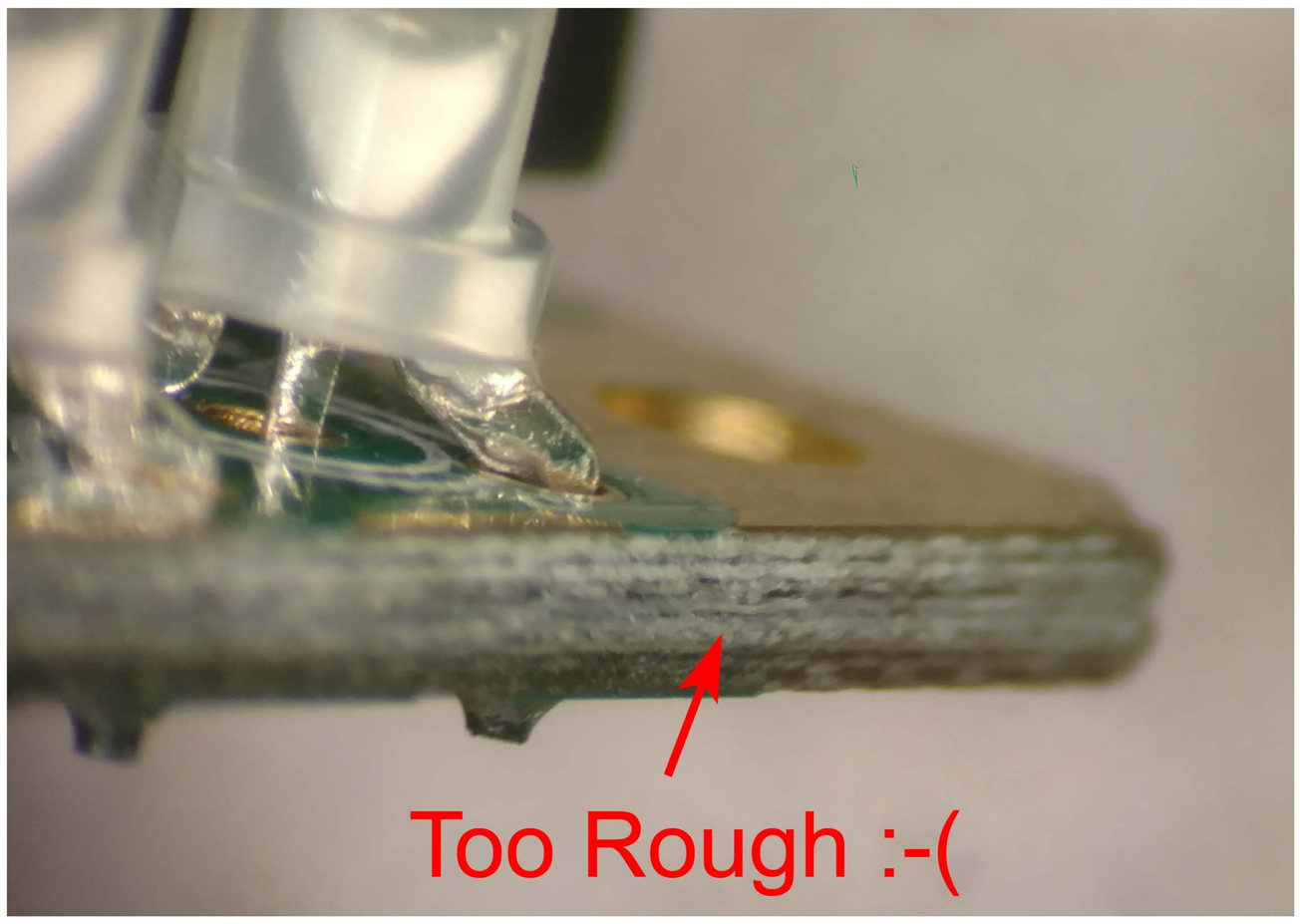
PROJECT CFA-215/6 VERSION 0v2
PCB PCB10082 REVISION 0v2
DATE 2017-03-25
LAYER Drill Drawing/Notes
FILE NAME C-CFA10082_0v2_DD.pho

Crystalfontz America, Incorporated

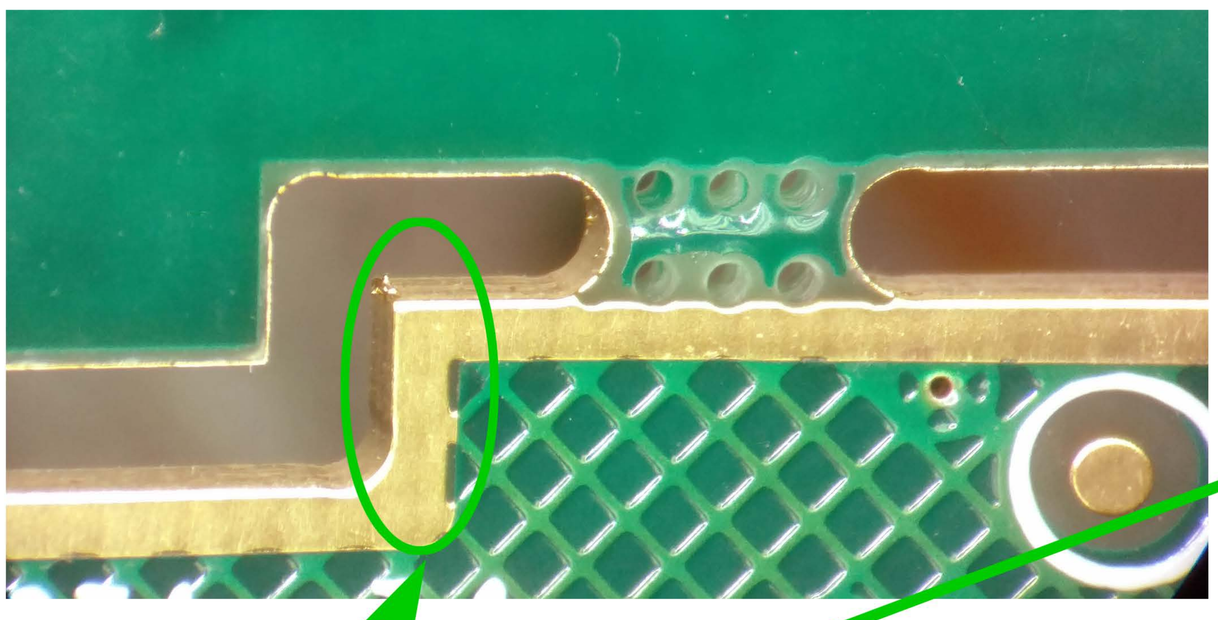
Crystalfontz PCB Edge Notes

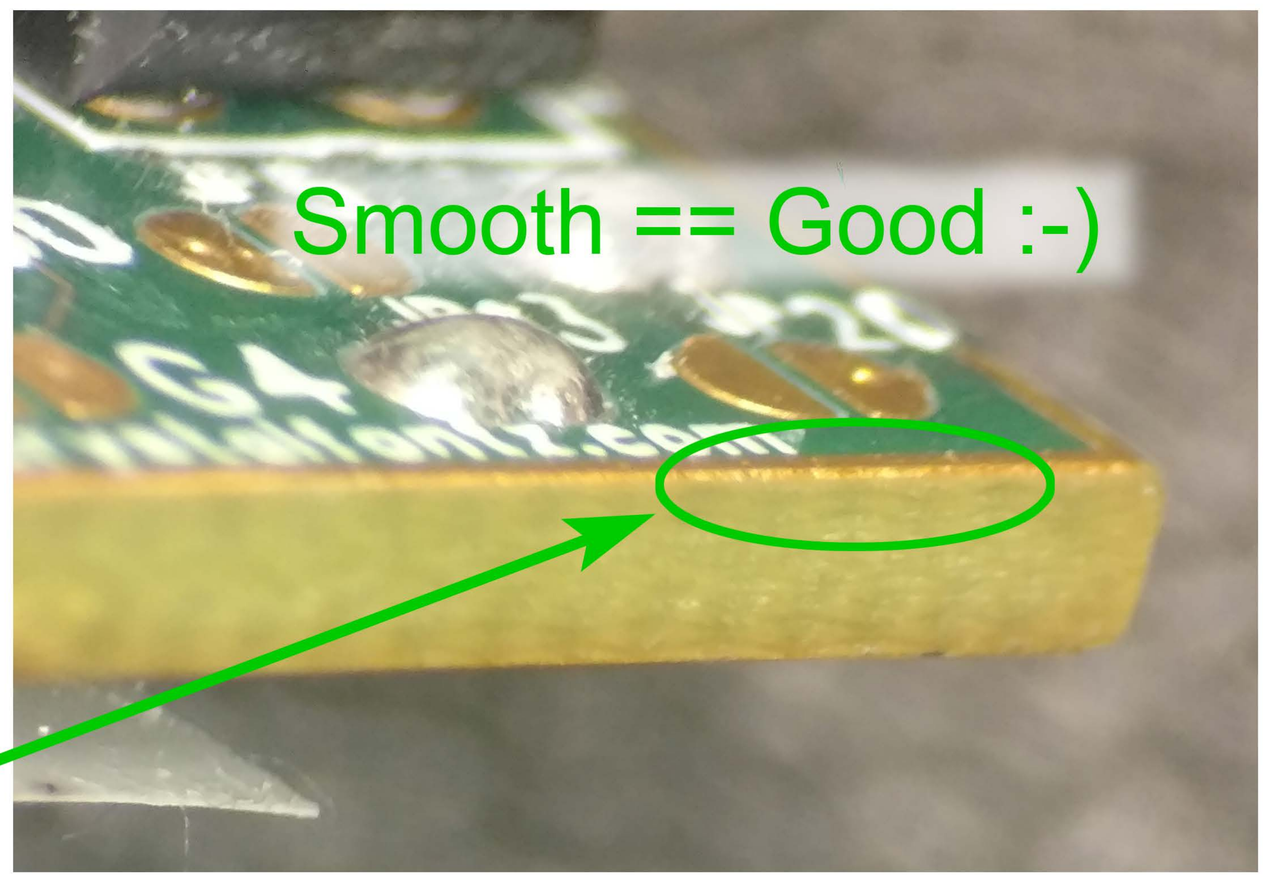
Do Not Use :-(



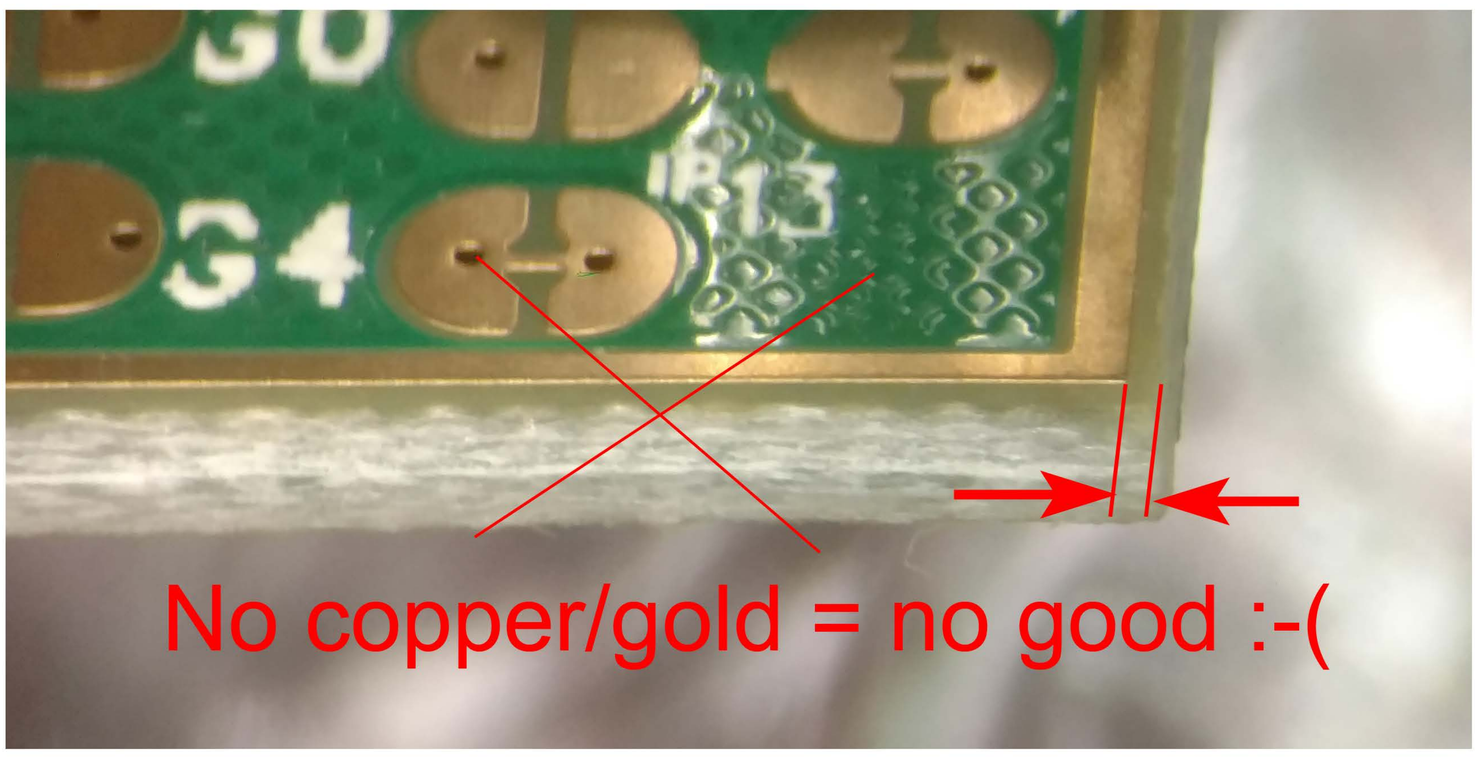


Use “router” :-)





Bare copper/gold all the way to the edge of the board :-)



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