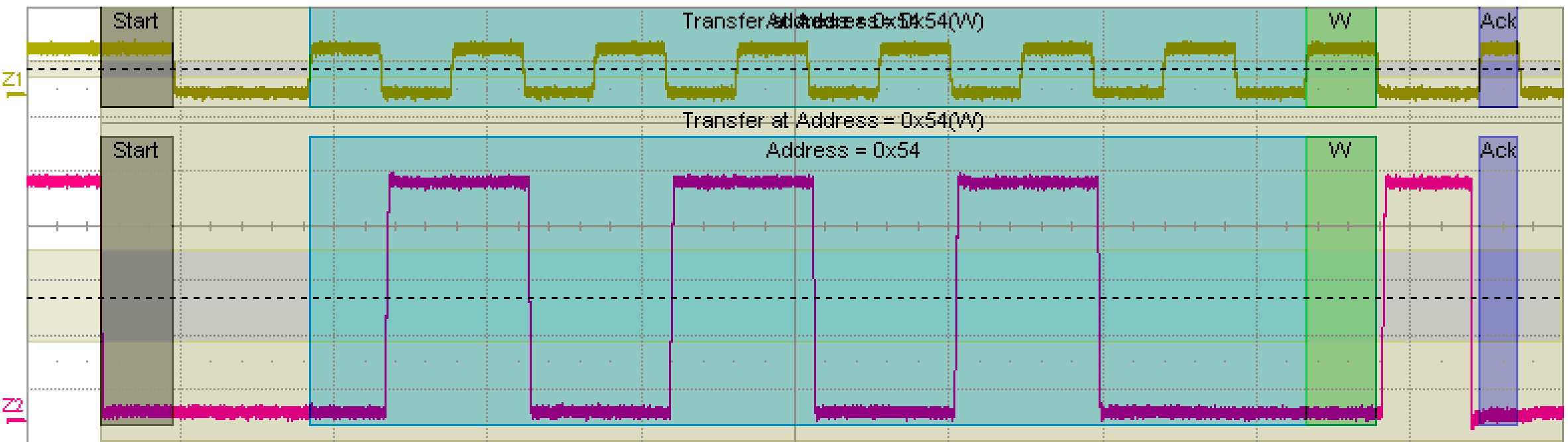
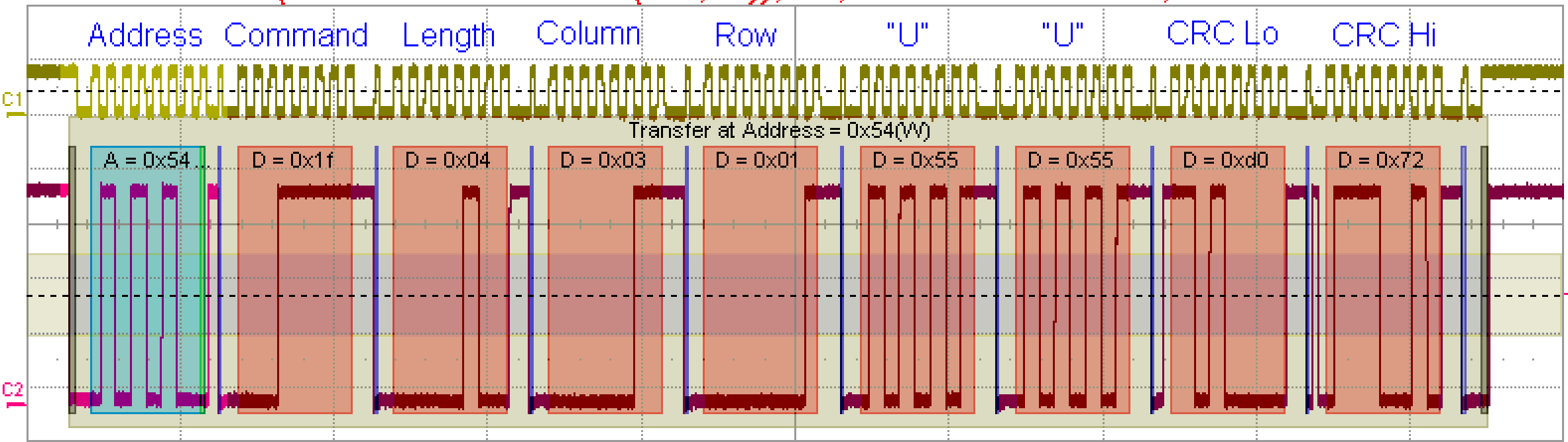


C=x1F(31 = Send Data to LCD (row,col)),L=4,D="x03x01x55x55",CRC=0x72D0



I2C	Time	Addr Length	Address	R/W	Length	Data	Status
1	-19.6846 ns	7	0x54	W	8	0x1f 04 03 01 55 55 d0 72	

C1 **DC1M** 5.00 V/div 10.00 V ofst

C2 **DC1M** 1.00 V/div -3.340 V ofst

Z1 zoom(C1) 4.50 V/div 10.8 μs/div

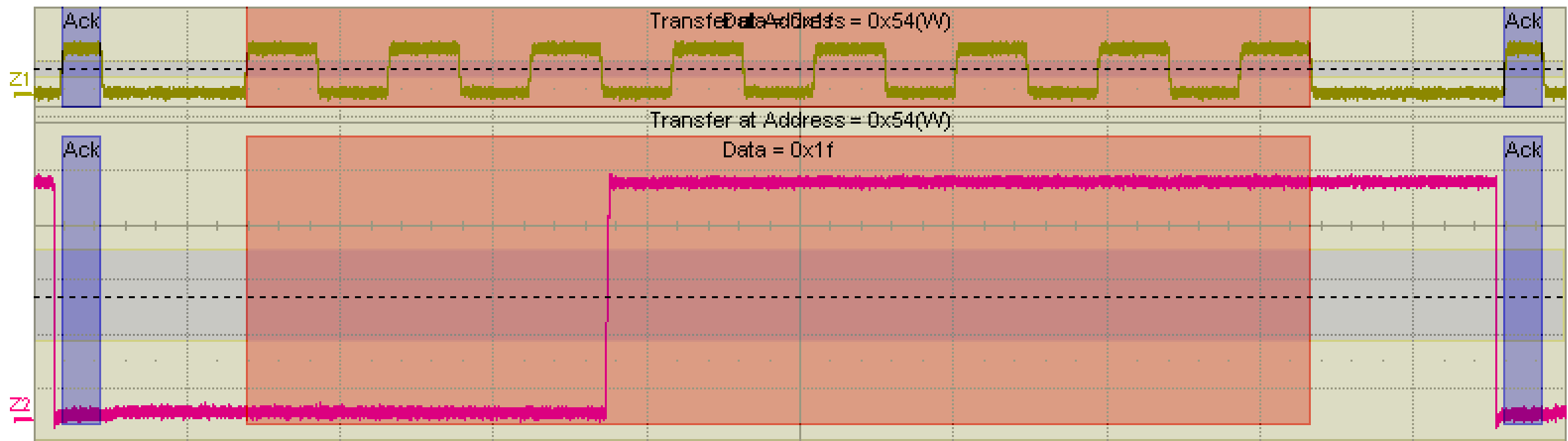
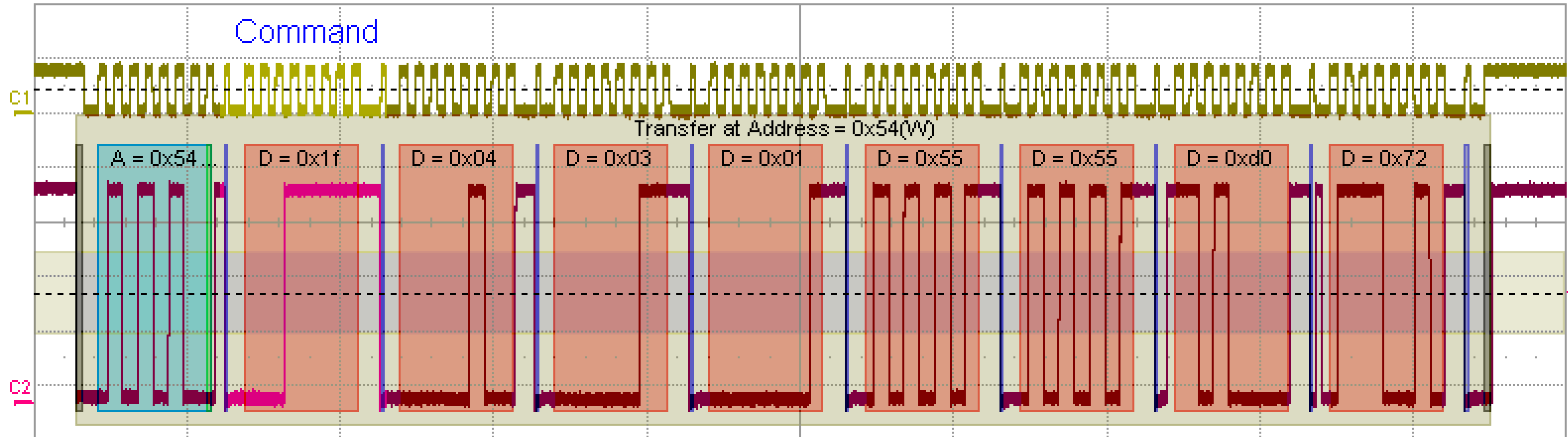
Z2 zoom(C2) 900 mV/div 10.8 μs/div

Timebase -472 μs 500 kS

100 μs/div 500 MS/s

Trigger **C2 DC** Stop 2.01 V Edge Negative

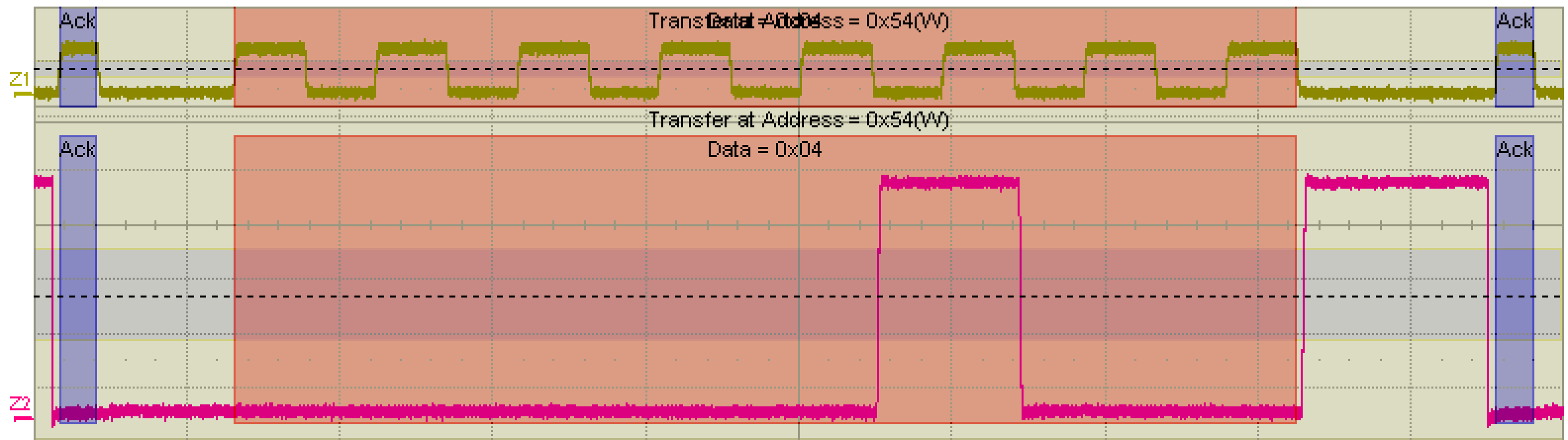
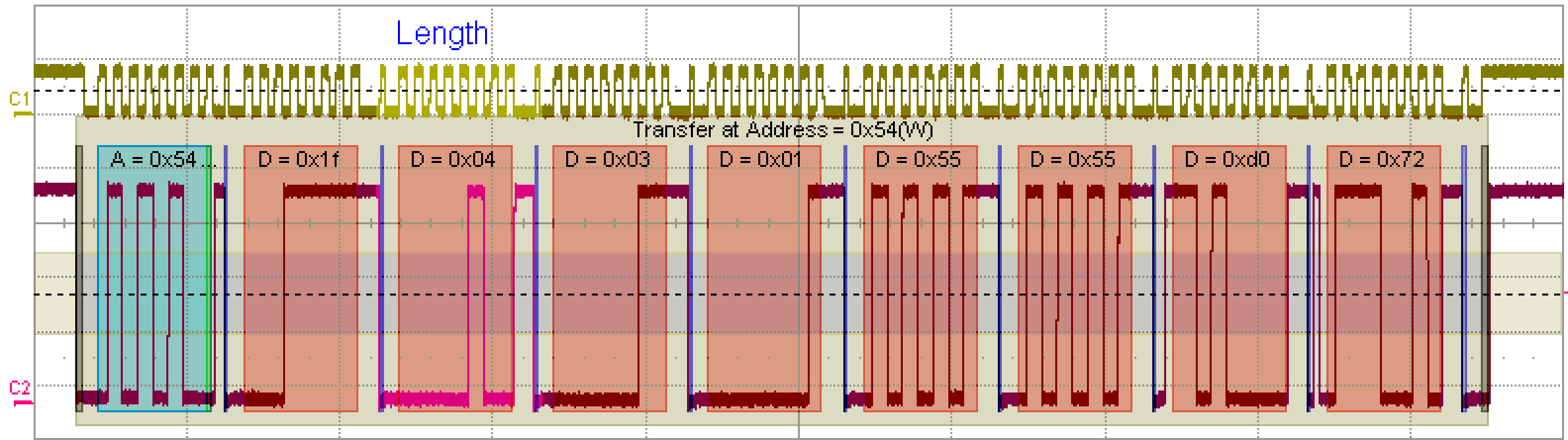
Command



I2C	Time	Addr Length	Address	R/W	Length	Data	Status
1	-19.6846 ns	7	0x54	W	8	0x1f 04 03 01 55 55 d0 72	

C1	DC1M	C2	DC1M	Z1	zoom(C1)	Z2	zoom(C2)	Timebase	-472 μ s	Trigger	C2 DC	
	5.00 V/div		1.00 V/div		4.50 V/div		900 mV/div		100 μ s/div		Stop	2.01 V
	10.00 V ofst		-3.340 V ofst		10.8 μ s/div		10.8 μ s/div		500 kS		Edge	Negative

Length



I2C	Time	Addr Length	Address	R/W	Length	Data	Status
1	-19.6846 ns	7	0x54	W	8	0x1f 04 03 01 55 55 d0 72	

C1 DCIM 5.00 V/div 10.00 V ofst

C2 DCIM 1.00 V/div -3.340 V ofst

Z1 zoom(C1) 4.50 V/div 10.8 μs/div

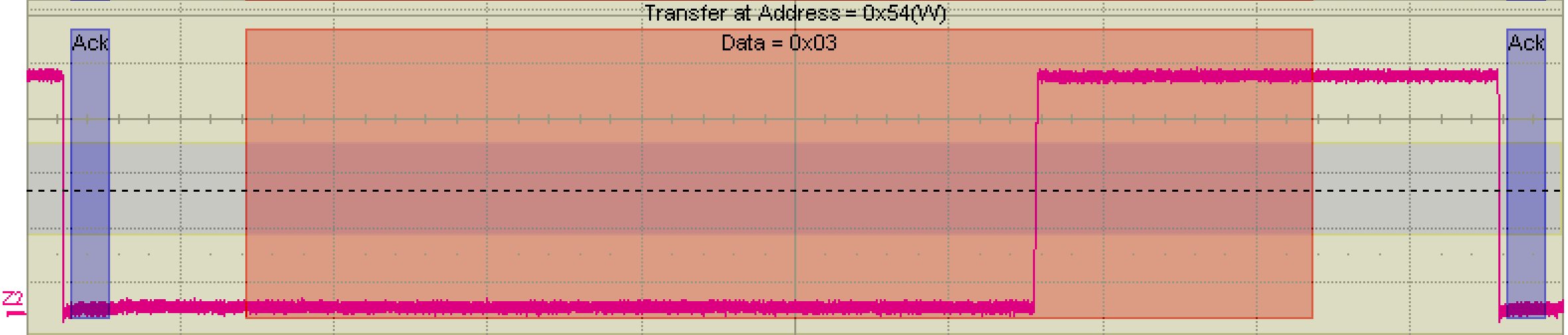
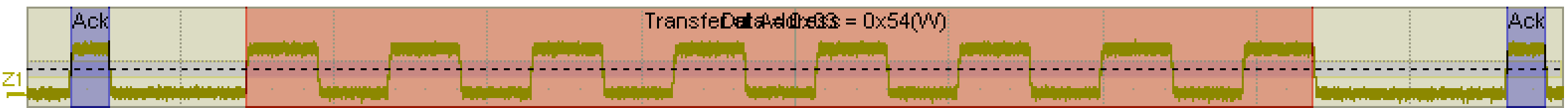
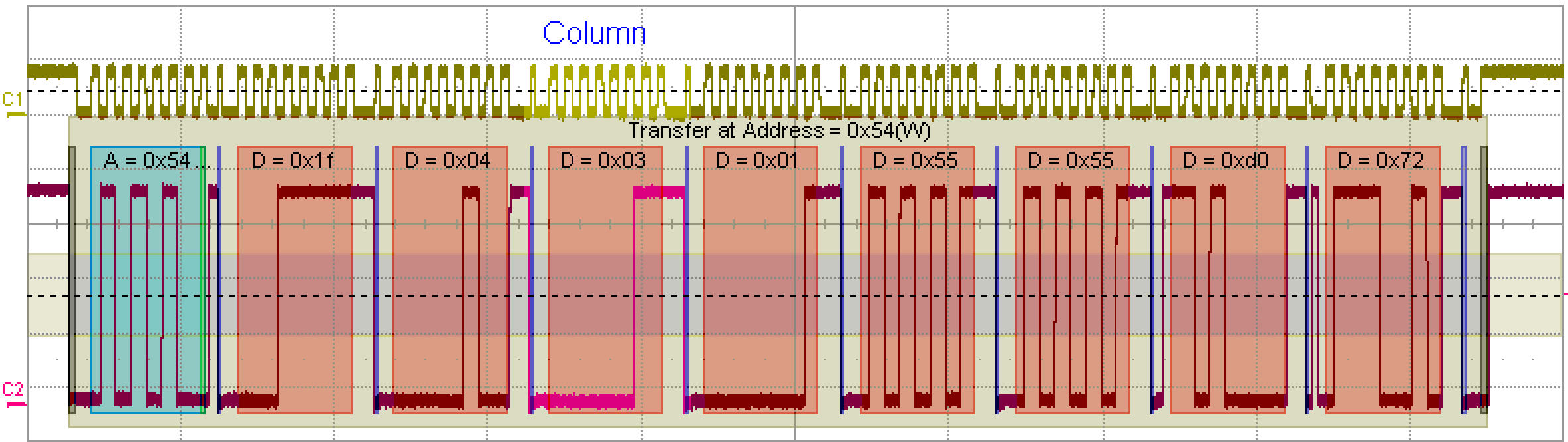
Z2 zoom(C2) 900 mV/div 10.8 μs/div

Timebase -472 μs 500 kS

100 μs/div 500 MS/s

Trigger C2 DC Stop 2.01 V Edge Negative

Column



I2C	Time	Addr Length	Address	R/W	Length	Data	Status
1	-19.6846 ns	7	0x54	W	8	0x1f 04 03 01 55 55 d0 72	

C1 DC1M 5.00 V/div 10.00 V ofst

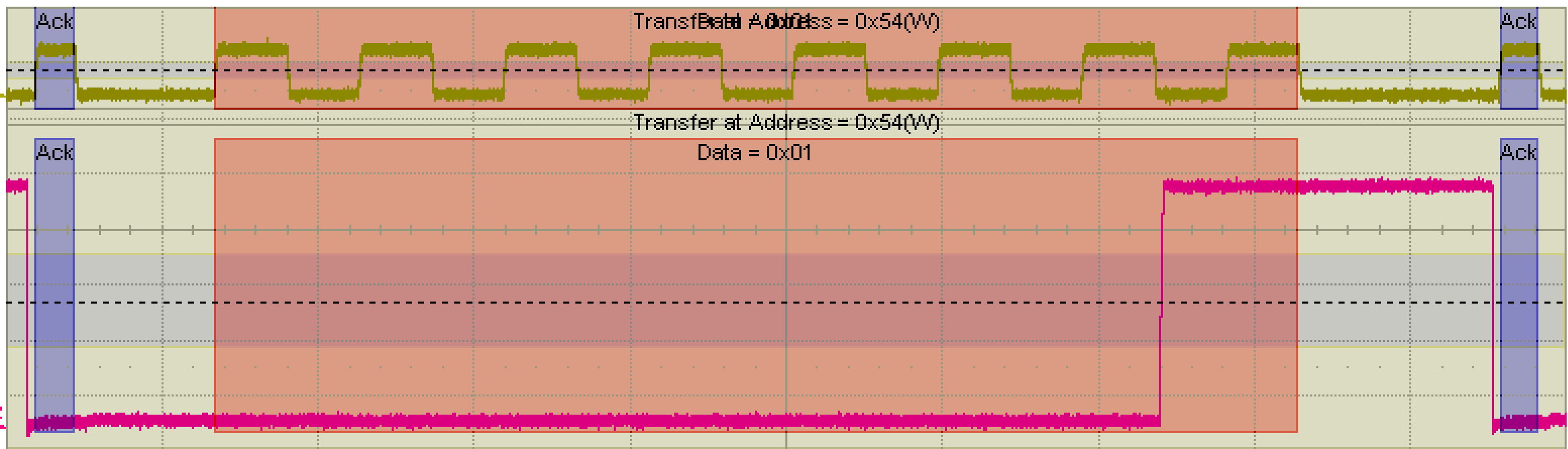
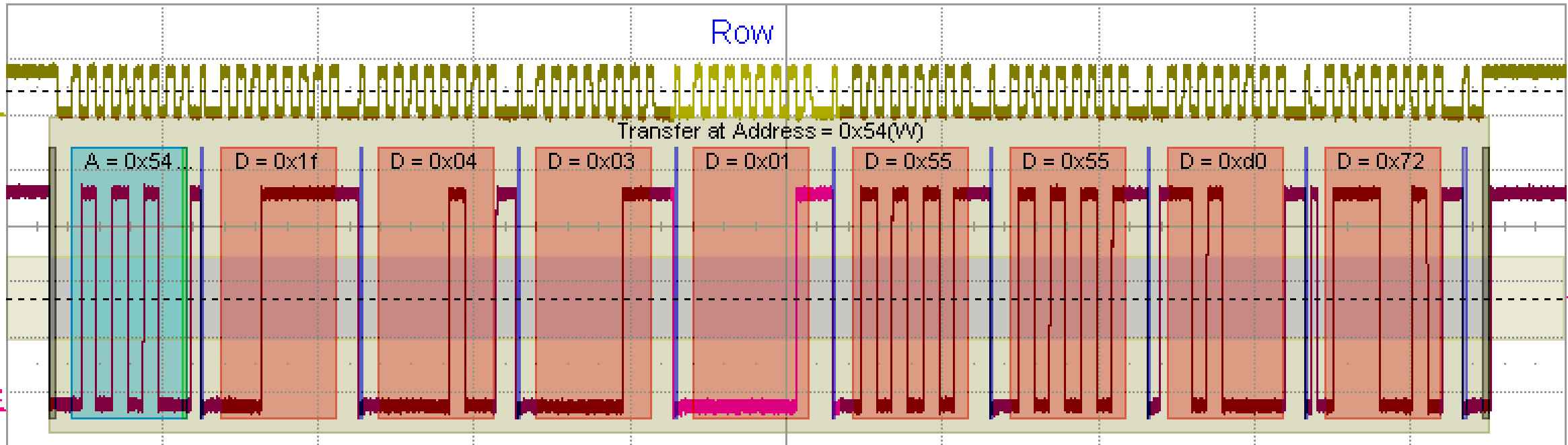
C2 DC1M 1.00 V/div -3.340 V ofst

Z1 zoom(C1) 4.50 V/div 10.8 μs/div

Z2 zoom(C2) 900 mV/div 10.8 μs/div

Timebase -472 μs 100 μs/div 500 kS

Trigger C2 DC Stop 2.01 V Edge Negative



I2C	Time	Addr Length	Address	R/W	Length	Data	Status
1	-19.6846 ns	7	0x54	W	8	0x1f 04 03 01 55 55 d0 72	

C1 DC1M 5.00 V/div 10.00 V ofst

C2 DC1M 1.00 V/div -3.340 V ofst

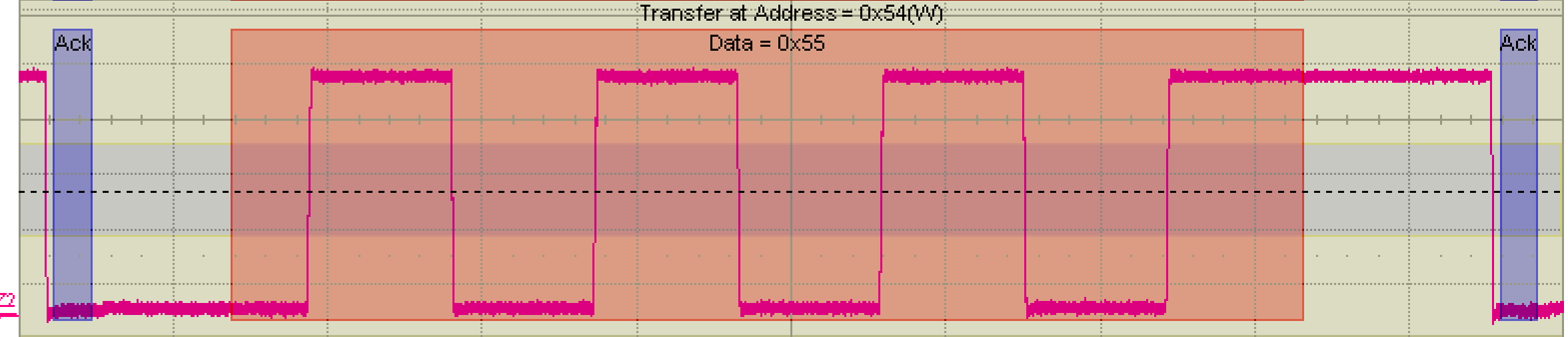
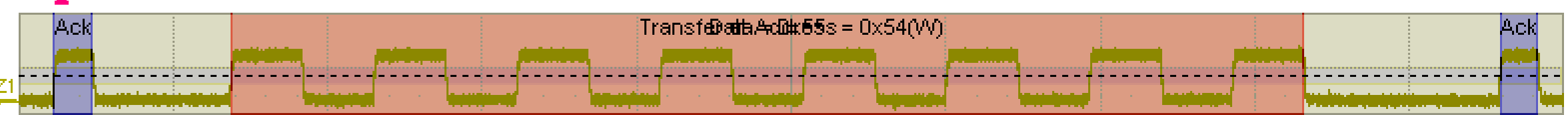
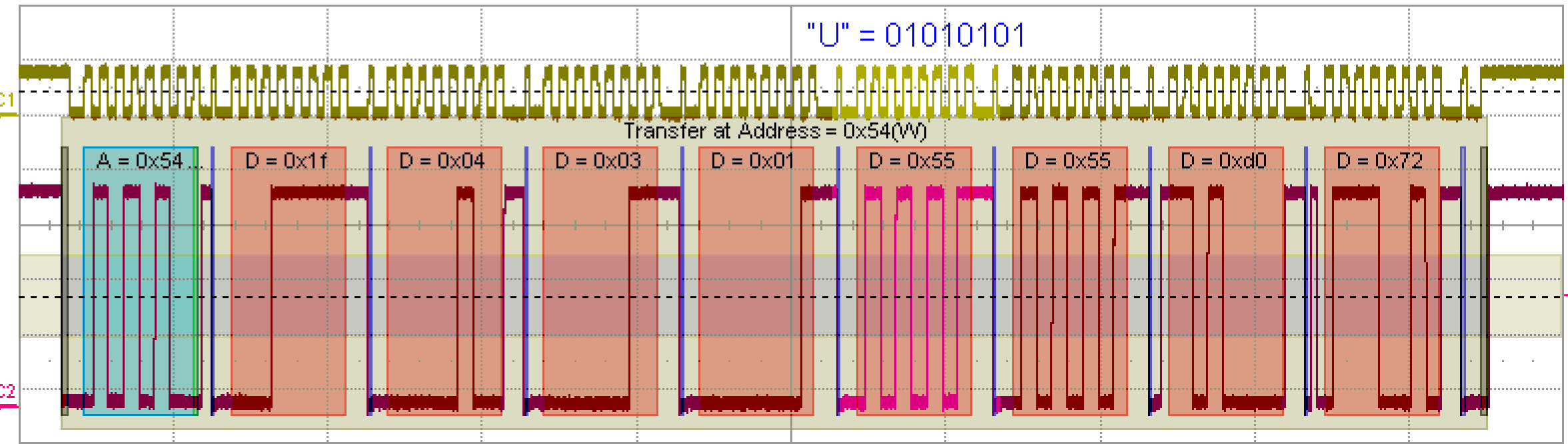
Z1 zoom(C1) 4.50 V/div 10.8 μs/div

Z2 zoom(C2) 900 mV/div 10.8 μs/div

Timebase -472 μs 100 μs/div 500 kS

Trigger C2 DC Stop 2.01 V Edge Negative

"U" = 01010101



I2C	Time	Addr Length	Address	R/W	Length	Data	Status
1	-19.6846 ns	7	0x54	W	8	0x1f 04 03 01 55 55 d0 72	

C1 DC1M 5.00 V/div 10.00 V ofst

C2 DC1M 1.00 V/div -3.340 V ofst

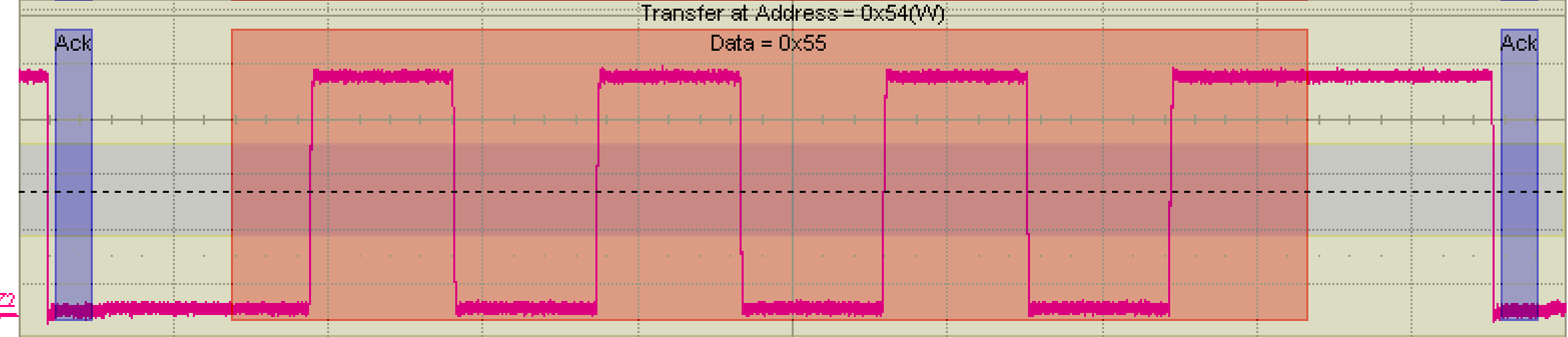
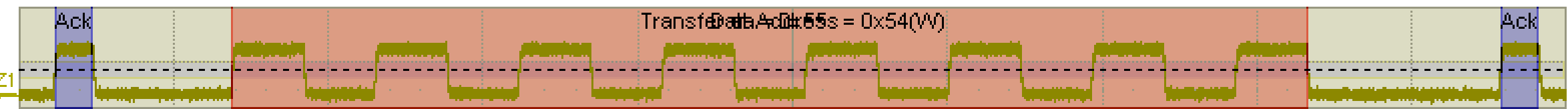
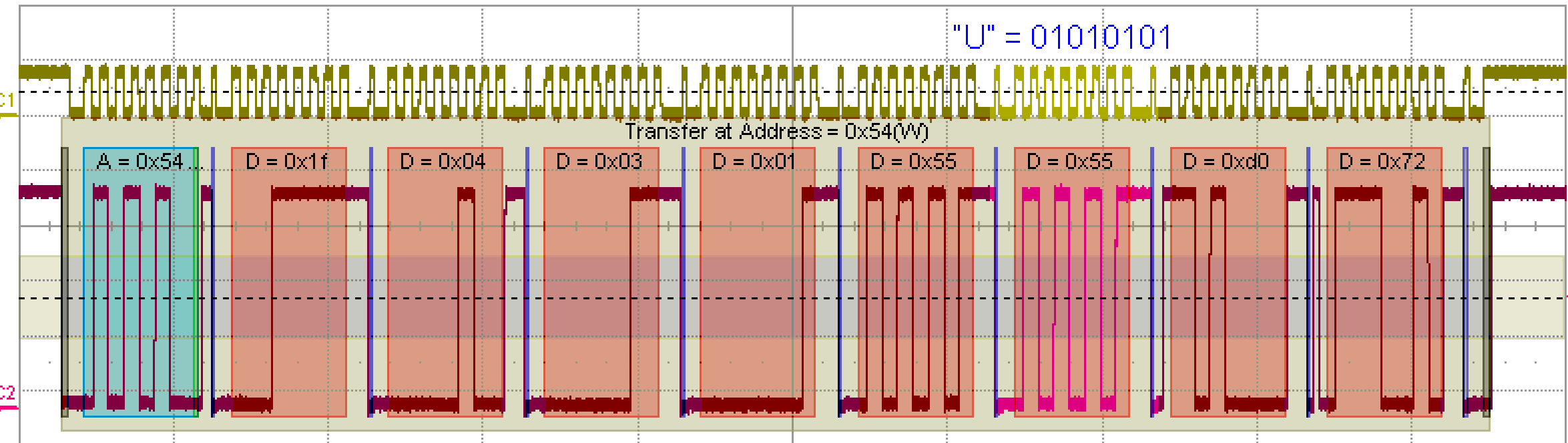
Z1 zoom(C1) 4.50 V/div 10.8 μs/div

Z2 zoom(C2) 900 mV/div 10.8 μs/div

Timebase -472 μs 100 μs/div 500 kS

Trigger C2 DC Stop 2.01 V Edge Negative

"U" = 01010101



I2C	Time	Addr Length	Address	R/W	Length	Data	Status
1	-19.6846 ns	7	0x54	W	8	0x1f 04 03 01 55 55 d0 72	

C1 DC1M 5.00 V/div 10.00 V ofst

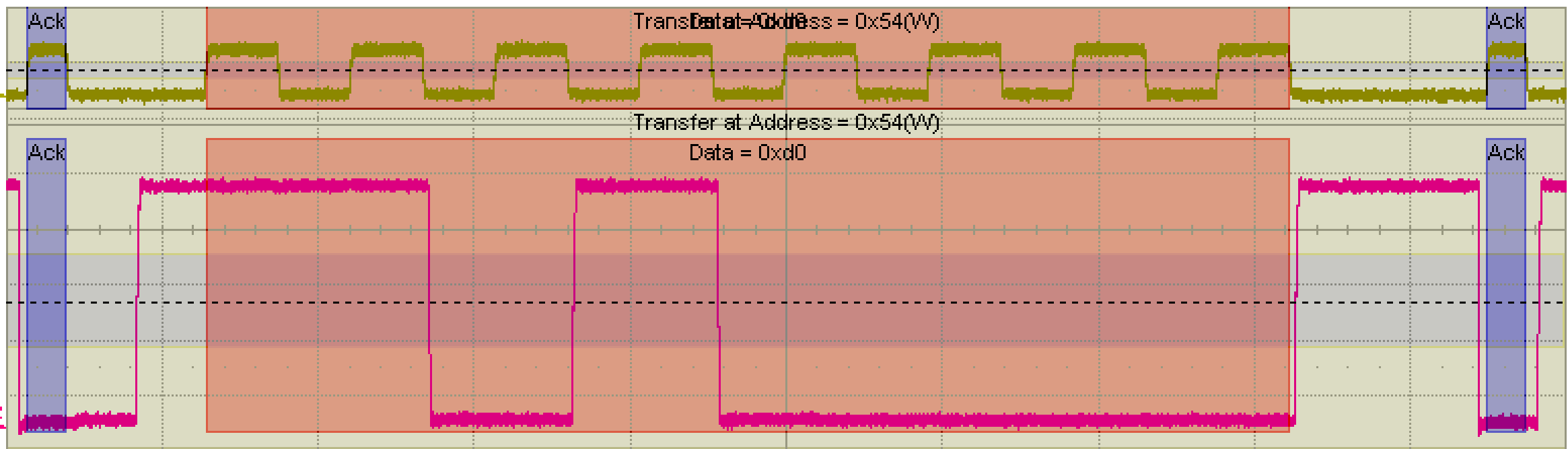
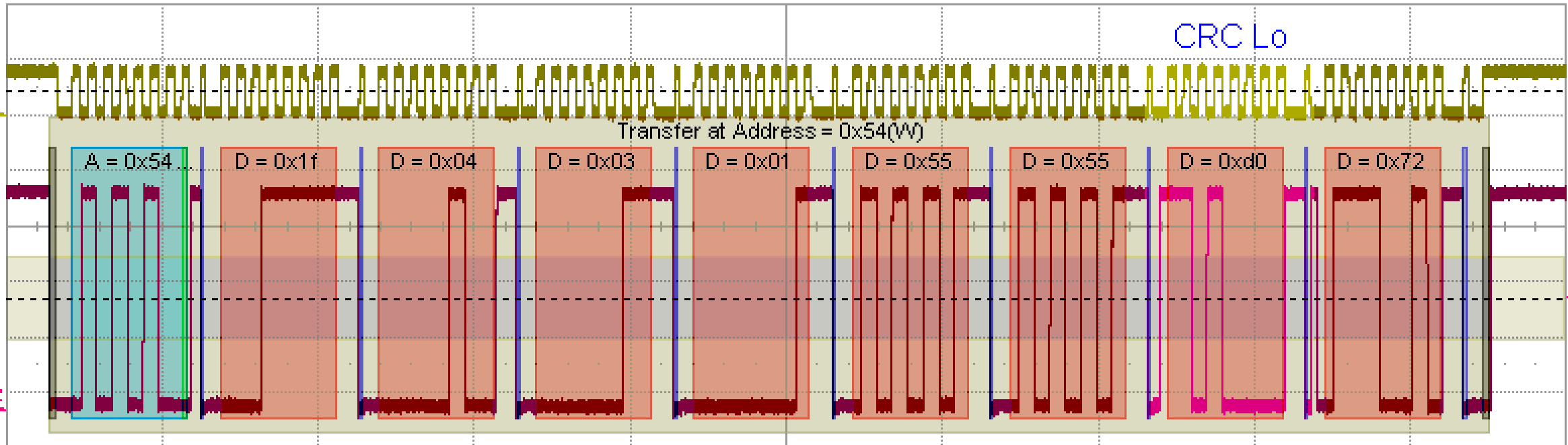
C2 DC1M 1.00 V/div -3.340 V ofst

Z1 zoom(C1) 4.50 V/div 10.8 μs/div

Z2 zoom(C2) 900 mV/div 10.8 μs/div

Timebase -472 μs 100 μs/div 500 kS

Trigger C2 DC Stop 2.01 V Edge Negative



I2C	Time	Addr Length	Address	R/W	Length	Data	Status
1	-19.6846 ns	7	0x54	W	8	0x1f 04 03 01 55 55 d0 72	

C1 **DC1M** 5.00 V/div 10.00 V ofst

C2 **DC1M** 1.00 V/div -3.340 V ofst

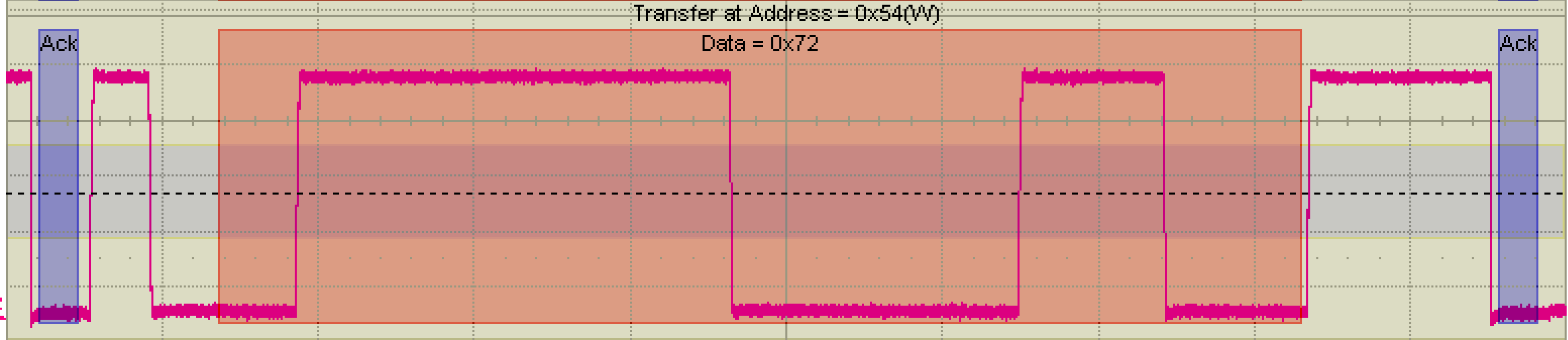
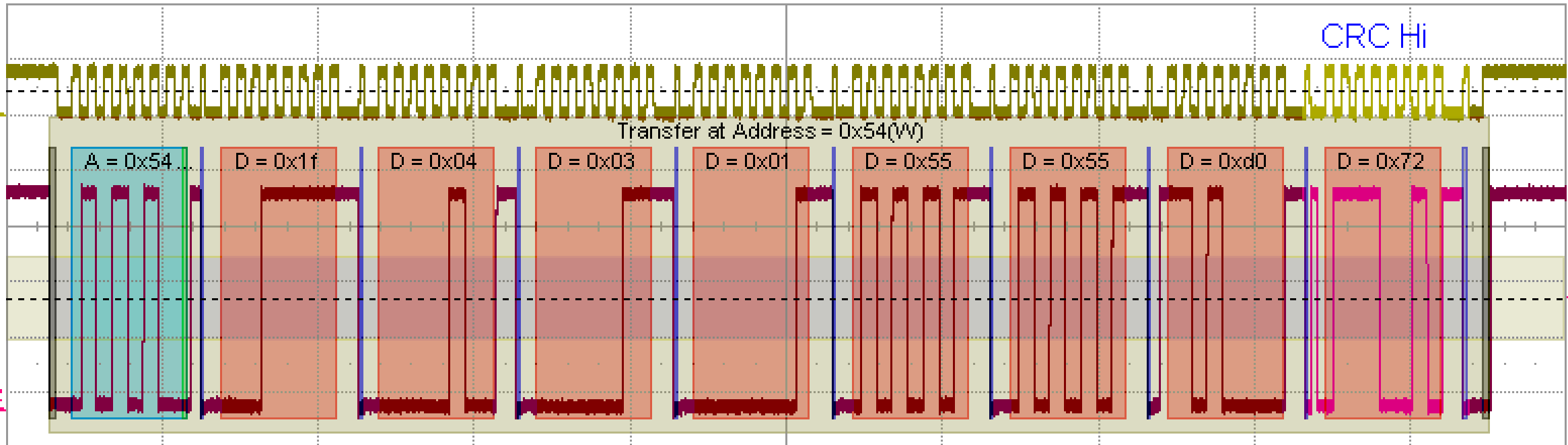
Z1 zoom(C1) 4.50 V/div 10.8 μs/div

Z2 zoom(C2) 900 mV/div 10.8 μs/div

Timebase -472 μs 500 kS

100 μs/div 500 MS/s

Trigger **C2 DC** Stop 2.01 V Edge Negative



I2C	Time	Addr Length	Address	R/W	Length	Data	Status
1	-19.6846 ns	7	0x54	W	8	0x1f 04 03 01 55 55 d0 72	

C1 DC1M 5.00 V/div 10.00 V ofst

C2 DC1M 1.00 V/div -3.340 V ofst

Z1 zoom(C1) 4.50 V/div 10.8 μs/div

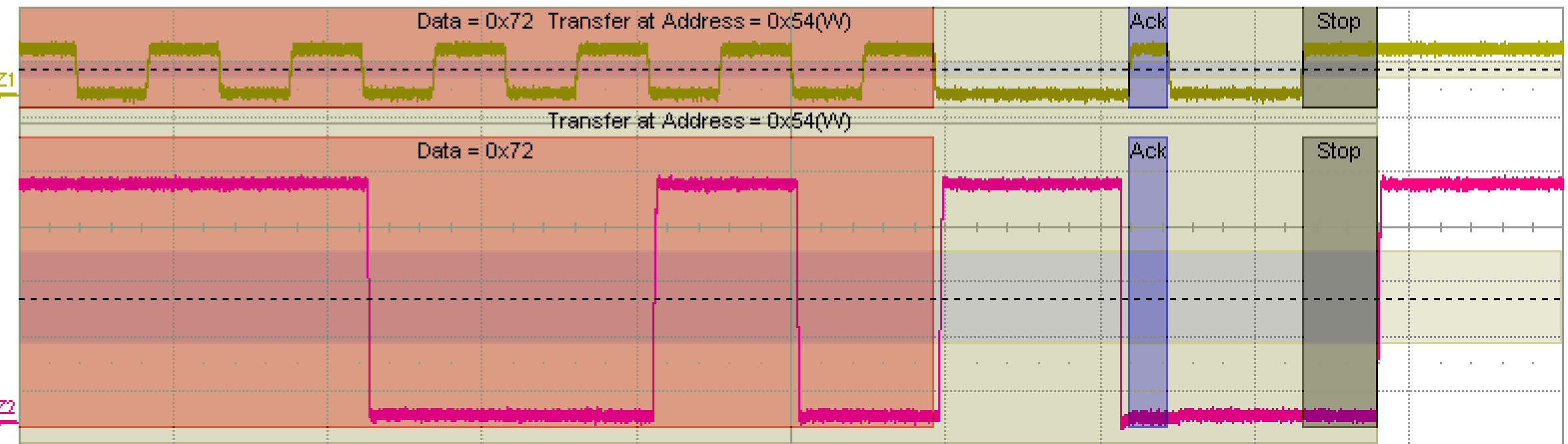
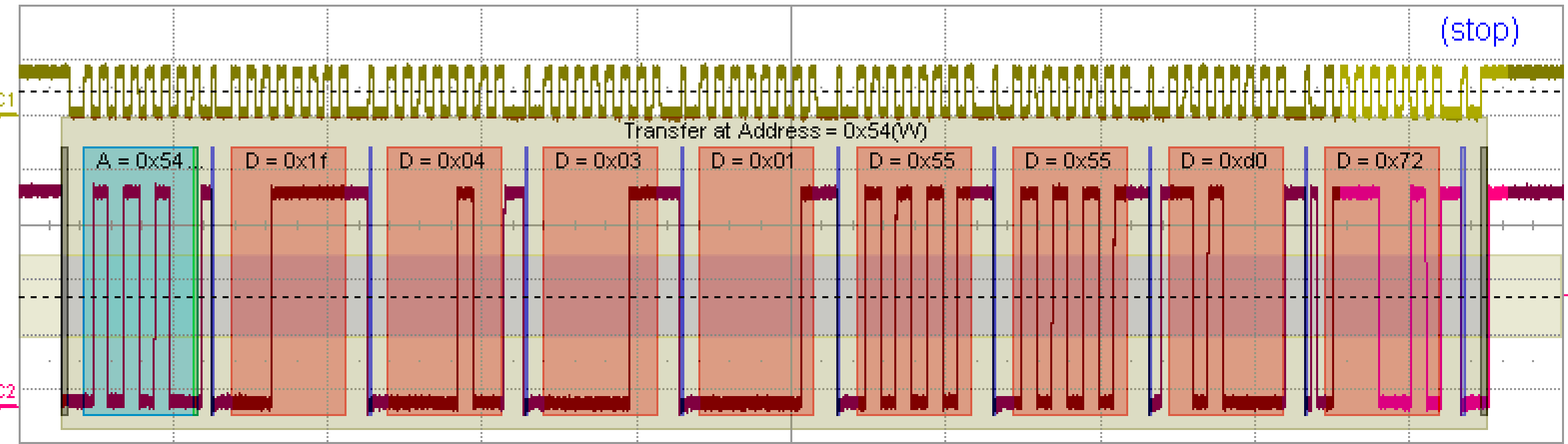
Z2 zoom(C2) 900 mV/div 10.8 μs/div

Timebase -472 μs 500 kS

100 μs/div 500 MS/s

Trigger C2 DC Stop 2.01 V Edge Negative

(stop)



I2C	Time	Addr Length	Address	R/W	Length	Data	Status
1	-19.6846 ns	7	0x54	W	8	0x1f 04 03 01 55 55 d0 72	

C1 **DC1M** 5.00 V/div 10.00 V ofst
 C2 **DC1M** 1.00 V/div -3.340 V ofst
 Z1 zoom(C1) 4.50 V/div 10.8 μs/div
 Z2 zoom(C2) 900 mV/div 10.8 μs/div

Timebase -472 μs 500 kS
 100 μs/div 500 MS/s
 Trigger **C2 DC**
 Stop 2.01 V
 Edge Negative